Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2599	(LPM or "local Packet memory")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:52
L2	0	1 and (access adj (port\$1 same contention))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:53
L3	22	1 and (port\$1 same contention)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:53
L4	17	3 and (status same delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:54
L5	0	4 and (read and write) same (controller\$1 near8 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:55
L6	0	4 and ((read and write) same (controller\$1 near8 access))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:56
L7	17	4 and read and write	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:56
L8	0	7 and (buffer same "memory cell")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:57

L9	2714	"370"/\$.ccls. and ("packet processing")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:57
L10	568	9 and (read and write and controller\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:58
L11	1	10 and "logic mechanism"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:58

Interference 69/924,755 Page 1/2

		mergerence	07/729		I	' 0
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	786	"370"/\$.ccls. and (packet adj process\$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:59
L2	87	"710"/\$.ccls. and (packet adj process\$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 13:59
L3	58	"711"/\$.ccls. and (packet adj process\$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:00
L4	32	1 and (controller near8 access).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:00
L5	9	2 and (controller near8 access).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:04
L6	12	3 and (controller near8 access).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:00
L7	0	4 and ((logic adj mechanism) same (buffer same write adj request)). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:02
L8	0	5 and ((logic adj mechanism) same (buffer same write adj request)). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:02

L9	0	6 and ((logic adj mechanism) same (buffer same write adj request)). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:03
L10	0	6 and (logic adj mechanism).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:03
L11	24357	"710".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:04
L12	1566	11 and (controller near8 access). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:04
L13	0	12 and (read adj request adj exists).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:05
L14	27742	"711"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:05
L15	25	14 and (read same (second adj controller)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:06
L16	0	15 and (request near5 delayed). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/08 14:06